IN THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1-41.(Canceled)

42.(Currently Amended) A display device comprising:

a semiconductor substrate;

an insulating layer formed on the semiconductor substrate;

a switching transistor and a current controlling transistor formed on the insulating layer, each comprising a source region, a drain region, a gate electrode and a gate insulating film;

a first interlayer insulating film over the switching transistor and the current controlling transistor;

a source wiring and a drain wiring which are connected with the switching transistor and a source wiring and a drain wiring which are connected with the current controlling transistor, and formed over the first interlayer insulating film;

[[an]] a second interlayer insulating film formed over the source wiring and the drain wiring of the switching transistor and the source wiring and the drain wiring of the current controlling transistor;

an electrode electrically connected with one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the switching transistor, and formed over the second interlayer insulating film;

a dielectric layer formed on the electrode;

a power supply line electrically connected with one of the source ~~region~~ wiring and the drain

~~region~~ wiring of the current controlling transistor, and formed on the dielectric layer;

a first electrode electrically connected with the other one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the current controlling transistor;

an EL layer formed over the first electrode; and

a second electrode formed over the EL layer.

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~~43~~.(Previously Presented) A display device according to claim ~~42~~, wherein the display device is incorporated in at least one selected from the group consisting of a portable telephone, a video camera, a mobile computer, a goggle type display, a projector, an electronic book, a digital camera, and a DVD player.

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~~44~~.(Previously Presented) A display device according to claim ~~42~~, wherein the first electrode overlaps the power supply line.

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~~45~~.(Previously Presented) A display device according to claim ~~42~~, wherein the electrode comprises one selected from the group consisting of Al, Ta and Ti.

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~~46~~.(Previously Presented) A display device according to claim ~~42~~, wherein the dielectric layer comprises an oxidation film of the electrode.

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~~47~~.(Currently Amended) A display device comprising:

a semiconductor substrate;

an insulating layer formed on the semiconductor substrate;

a p-channel type switching transistor and an n-channel type current controlling transistor formed on the insulating layer, each comprising a source region, a drain region, a gate electrode and a gate insulating film;

a first interlayer insulating film over the p-channel type switching transistor and the n-channel type current controlling transistor;

a source wiring and a drain wiring which are connected with the p-channel type switching transistor and a source wiring and a drain wiring which are connected with the n-channel type current controlling transistor, and formed over the first interlayer insulating film;

[[an]] a second interlayer insulating film formed over the source wiring and the drain wiring of the p-channel type switching transistor and the source wiring and the drain wiring of the n-channel type current controlling transistor;

an electrode electrically connected with one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the p-channel type switching transistor, and formed over the second interlayer insulating film;

a dielectric layer formed on the electrode;

a power supply line electrically connected with one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the n-channel type current controlling transistor, and formed on the dielectric layer;

a first electrode electrically connected with the other one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the n-channel type current controlling transistor;

an EL layer formed over the first electrode; and

a second electrode formed over the EL layer.

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~~48.~~(Previously Presented) A display device according to claim ~~47~~, wherein the first electrode overlaps the power supply line.

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~~49.~~(Previously Presented) A display device according to claim ~~47~~, wherein the electrode comprises one selected from the group consisting of Al, Ta and Ti.

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~~50.~~(Previously Presented) A display device according to claim ~~47~~, wherein the dielectric layer comprises an oxidation film of the electrode.

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~~51.~~(Previously Presented) A display device according to claim ~~47~~, wherein the display device is incorporated in at least one selected from the group consisting of a portable telephone, a video camera, a mobile computer, a goggle type display, a projector, an electronic book, a digital camera, and a DVD player.

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~~52.~~(Currently Amended) A display device comprising:

a semiconductor substrate;

a switching transistor and a current controlling transistor formed on the semiconductor substrate, each comprising a source region, a drain region, a gate electrode and a gate insulating film;

a first interlayer insulating film over the switching transistor and the current controlling transistor;

a source wiring and a drain wiring which are connected with the switching transistor and a

source wiring and a drain wiring which are connected with the current controlling transistor, and formed over the first interlayer insulating film;

[[an]] a second interlayer insulating film formed over the source wiring and the drain wiring of the switching transistor and the source wiring and the drain wiring of the current controlling transistor;

an electrode electrically connected with one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the switching transistor, and formed over the second interlayer insulating film;

a dielectric layer formed on the electrode;

a power supply line electrically connected with one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the current controlling transistor, and formed on the dielectric layer;

a first electrode electrically connected with the other one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the current controlling transistor;

an EL layer formed over the first electrode; and

a second electrode formed over the EL layer.

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53.(Previously Presented) A display device according to claim 52, wherein the first electrode overlaps the power supply line.

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54.(Previously Presented) A display device according to claim 52, wherein the electrode comprises one selected from the group consisting of Al, Ta and Ti.

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~~55~~.(Previously Presented) A display device according to claim ~~52~~, wherein the dielectric layer comprises an oxidation film of the electrode.

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~~56~~.(Previously Presented) A display device according to claim ~~52~~, wherein the display device is incorporated in at least one selected from the group consisting of a portable telephone, a video camera, a mobile computer, a goggle type display, a projector, an electronic book, a digital camera, and a DVD player.

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~~57~~.(Currently Amended) A display device comprising:

a semiconductor substrate;

a p-channel type switching transistor and an n-channel type current controlling transistor formed on the semiconductor substrate, each comprising a source region, a drain region, a gate electrode and a gate insulating film;

a first interlayer insulating film over the p-channel type switching transistor and the n-channel type current controlling transistor;

a source wiring and a drain wiring which are connected with the p-channel type switching transistor and a source wiring and a drain wiring which are connected with the n-channel type current controlling transistor, and formed over the first interlayer insulating film;

[[an]] a second interlayer insulating film formed over the source wiring and the drain wiring of the p-channel type switching transistor and the source wiring and the drain wiring of the n-channel type current controlling transistor;

an electrode electrically connected with one of the source ~~region~~ wiring and the drain ~~region~~

wiring of the p-channel type switching transistor, and formed over the second interlayer insulating film;

a dielectric layer formed on the electrode;

a power supply line electrically connected with one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the n-channel type current controlling transistor, and formed on the dielectric layer;

a first electrode electrically connected with the other one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the n-channel type current controlling transistor;

an EL layer formed over the first electrode; and

a second electrode formed over the EL layer.

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~~58~~.(Previously Presented) A display device according to claim ~~57~~, wherein the first electrode overlaps the power supply line.

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~~59~~.(Previously Presented) A display device according to claim ~~57~~, wherein the electrode comprises one selected from the group consisting of Al, Ta and Ti.

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~~60~~.(Previously Presented) A display device according to claim ~~57~~, wherein the dielectric layer comprises an oxidation film of the electrode.

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~~61~~.(Previously Presented) A display device according to claim ~~57~~, wherein the display device is incorporated in at least one selected from the group consisting of a portable telephone, a video camera, a mobile computer, a goggle type display, a projector, an electronic book, a digital camera, and a DVD player.

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~~62~~.(Currently Amended) A display device comprising:

a semiconductor substrate;

a switching transistor and a current controlling transistor formed on the semiconductor substrate, each comprising a source region, a drain region, a gate electrode and a gate insulating film;

a first interlayer insulating film over the switching transistor and the current controlling transistor;

a source wiring and a drain wiring which are connected with the switching transistor and a source wiring and a drain wiring which are connected with the current controlling transistor, and formed over the first interlayer insulating film;

[[an]] a second interlayer insulating film formed over the source wiring and the drain wiring of the switching transistor and the source wiring and the drain wiring of the current controlling transistor;

an electrode electrically connected with one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the switching transistor, and formed over the second interlayer insulating film;

a dielectric layer formed on the electrode;

a power supply line electrically connected with one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the current controlling transistor, and formed on the dielectric layer;

a storage capacitance comprising the electrode, the dielectric layer and the power supply line;

a first electrode electrically connected with the other one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the current controlling transistor;

an EL layer formed over the first electrode; and

a second electrode formed over the EL layer.

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~~63~~.(Previously Presented) A display device according to claim ~~62~~, wherein the first electrode overlaps the power supply line.

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~~64~~.(Previously Presented) A display device according to claim ~~62~~, wherein the electrode comprises one selected from the group consisting of Al, Ta and Ti.

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~~65~~.(Previously Presented) A display device according to claim ~~62~~, wherein the dielectric layer comprises an oxidation film of the electrode.

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~~66~~.(Previously Presented) A display device according to claim ~~62~~, wherein the display device is incorporated in at least one selected from the group consisting of a portable telephone, a video camera, a mobile computer, a goggle type display, a projector, an electronic book, a digital camera, and a DVD player.

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~~67~~.(Currently Amended) A display device comprising:

a semiconductor substrate;

a p-channel type switching transistor and an n-channel type current controlling transistor formed on the semiconductor substrate, each comprising a source region, a drain region, a gate electrode and a gate insulating film;

a first interlayer insulating film over the p-channel type switching transistor and the n-channel type current controlling transistor;

a source wiring and a drain wiring which are connected with the p-channel type switching transistor and a source wiring and a drain wiring which are connected with the n-channel type current controlling transistor, and formed over the first interlayer insulating film;

[[an]] a second interlayer insulating film formed over the source wiring and the drain wiring of the p-channel type switching transistor and the source wiring and the drain wiring of the n-channel type current controlling transistor;

an electrode electrically connected with one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the p-channel type switching transistor, and formed over the second interlayer insulating film;

a dielectric layer formed on the electrode;

a power supply line electrically connected with one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the n-channel type current controlling transistor, and formed on the dielectric layer;

a storage capacitance comprising the electrode, the dielectric layer and the power supply line;

a first electrode electrically connected with the other one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the n-channel type current controlling transistor;

an EL layer formed over the first electrode; and

a second electrode formed over the EL layer.

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68.(Previously Presented) A display device according to claim 67, wherein the first electrode overlaps the power supply line.

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69.(Previously Presented) A display device according to claim 67, wherein the electrode comprises one selected from the group consisting of Al, Ta and Ti.

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70.(Previously Presented) A display device according to claim 67, wherein the dielectric layer comprises an oxidation film of the electrode.

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71.(Previously Presented) A display device according to claim 67, wherein the display device is incorporated in at least one selected from the group consisting of a portable telephone, a video camera, a mobile computer, a goggle type display, a projector, an electronic book, a digital camera, and a DVD player.

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72.(Previously Presented) A display device according to claim 42, wherein the EL layer is organic.

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73.(Previously Presented) A display device according to claim 47, wherein the EL layer is organic.

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~~74.~~(Previously Presented) A display device according to claim ~~52~~, wherein the EL layer is organic.

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~~75.~~(Previously Presented) A display device according to claim ~~57~~, wherein the EL layer is organic.

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~~76.~~(Previously Presented) A display device according to claim ~~62~~, wherein the EL layer is organic.

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~~77.~~(Previously Presented) A display device according to claim ~~67~~, wherein the EL layer is organic.